



HW

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Ga Won LEE

Serial No.: 10/811,152

Group Art Unit: NOT YET ASSIGNED

Filed: May 28, 2003

Examiner: NOT YET ASSIGNED

For: METHOD FOR FORMING INTER-DIELECTRIC LAYER IN SEMICONDUCTOR

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TRANSMITTAL

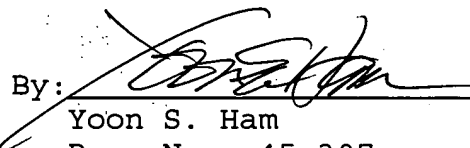
Honorable Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

July 15, 2004

Sir:

Enclosed herewith is a New Power of Attorney and Change of Correspondence Address in connection with the above-identified application.

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Atty. Docket: P69832US1  
YSH:jy